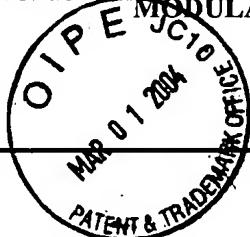| **TRANSMITTAL OF FORMAL DRAWINGS**	Docket No. BUR920030161US1 (17103)

In Re Application Of: **Darren L. Anand, et al**

Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
10/707,067	November 19, 2003	Unknown	Unknown	Unknown

Invention: **MODULAR DLL ARCHITECTURE FOR GENERATING MULTIPLE TIMINGS**

Address to:
**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

Transmitted herewith are:

    3   sheets of formal drawing(s) for this application.

☒   Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).

_____
               Signature

**Steven Fischman
Registration No. 34,594**

Dated: **February 26, 2004**

Correspondence Address
Customer No.: 23389